

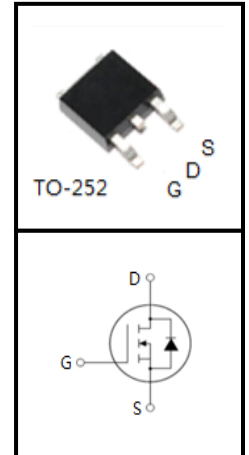
600V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information

Device	Package	Marking
CS1N60D	TO-252	CS1N60D

Absolute Maximum Ratings $T_C = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS} = 0\text{V}$)	V_{DSS}	600	V
Continuous Drain Current	I_D	1.3	A
Pulsed Drain Current (note1)	I_{DM}	5.2	A
Gate-Source Voltage	V_{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E_{AS}	7.2	mJ
Avalanche Current (note1)	I_{AS}	1.2	A
Repetitive Avalanche Energy (note1)	E_{AR}	0.029	mJ
Power Dissipation ($T_C = 25^{\circ}\text{C}$)	P_D	27.5	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^{\circ}\text{C}$

Thermal Resistance

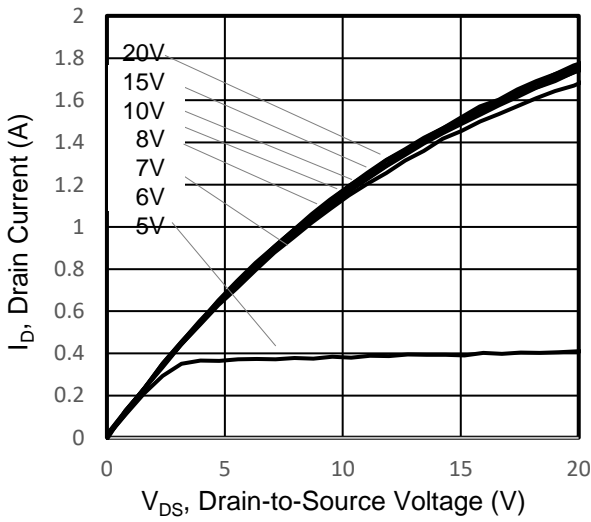
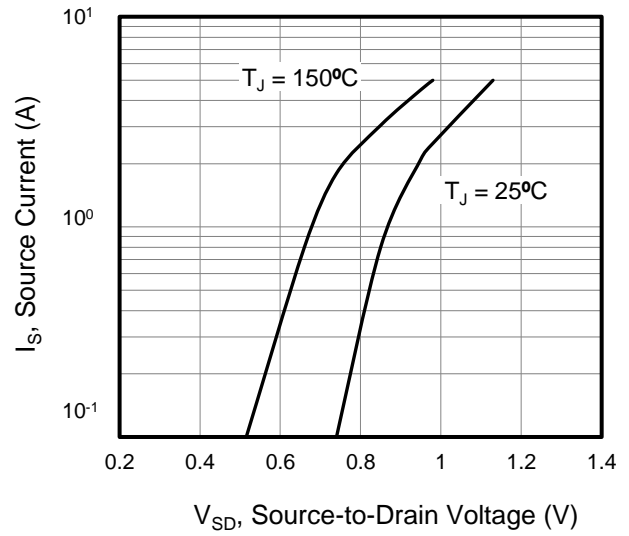
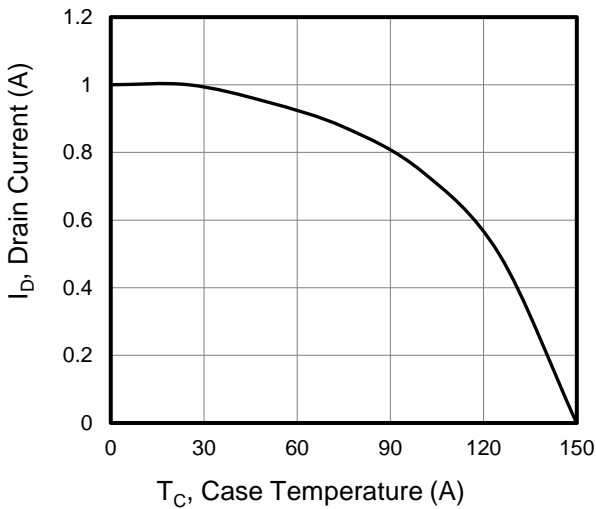
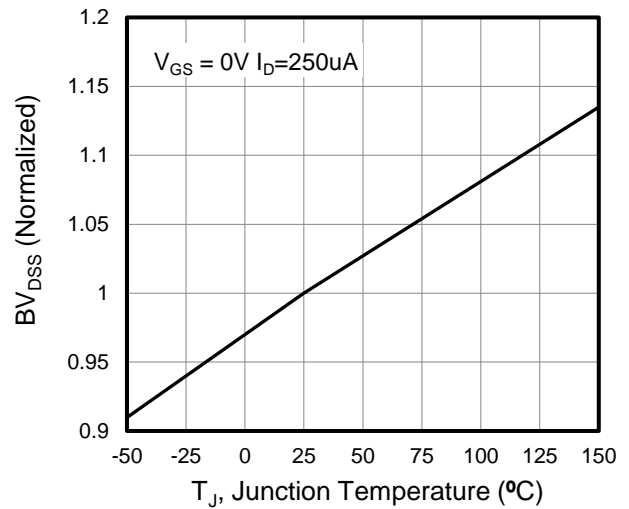
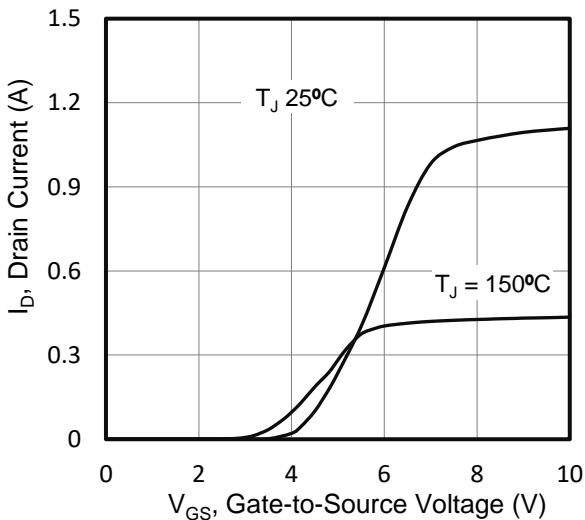
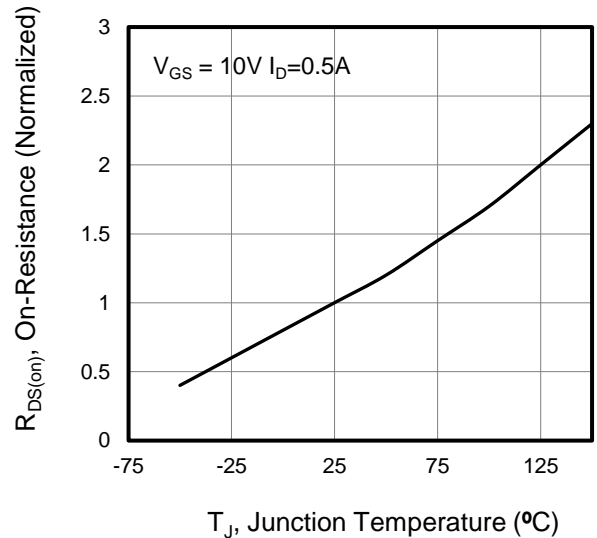
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	4.53	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	105	

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 30V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	3.0	--	4.2	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 0.5A$	--	7.0	8.0	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$ $f = 1.0\text{MHz}$	--	131	--	pF
Output Capacitance	C_{oss}		--	16.5	--	
Reverse Transfer Capacitance	C_{rss}		--	2.5	--	
Total Gate Charge	Q_g	$V_{DD} = 480V, I_D = 1.3A,$ $V_{GS} = 10V$	--	6.3	--	nC
Gate-Source Charge	Q_{gs}		--	0.8	--	
Gate-Drain Charge	Q_{gd}		--	4.2	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 300V, I_D = 1.3A,$ $R_G = 25\Omega$	--	32.5	--	ns
Turn-on Rise Time	t_r		--	6.5	--	
Turn-off Delay Time	$t_{d(off)}$		--	42.5	--	
Turn-off Fall Time	t_f		--	43	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	1.3	A
Pulsed Diode Forward Current	I_{SM}		--	--	5.2	
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = 0.5A, V_{GS} = 0V$	--	--	1.4	V
Reverse Recovery Time	t_{rr}	$V_{DD} = 300V, I_S = 1.3A,$ $di_F/dt = 100A/\mu s$	--	3.8	--	ns
Reverse Recovery Charge	Q_{rr}		--	0.53	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L=10\text{mH}, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

Figure 2. Body Diode Forward Voltage

Figure 3. Drain Current vs. Temperature

Figure 4. BV_{DSS} Variation vs. Temperature

Figure 5. Transfer Characteristics

Figure 6. On-Resistance vs. Temperature


Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

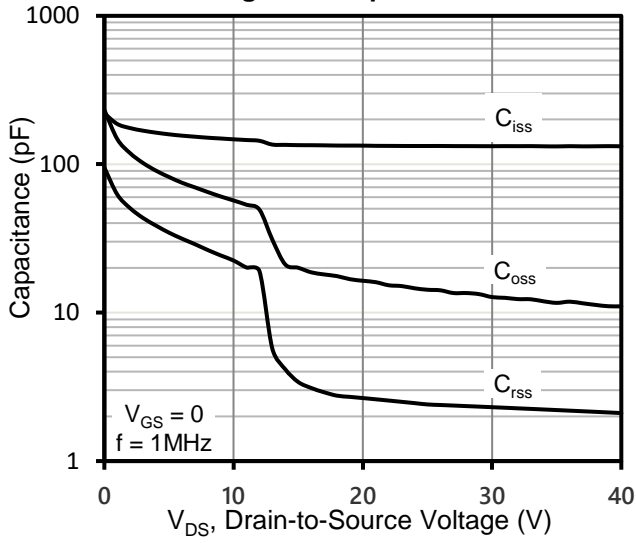
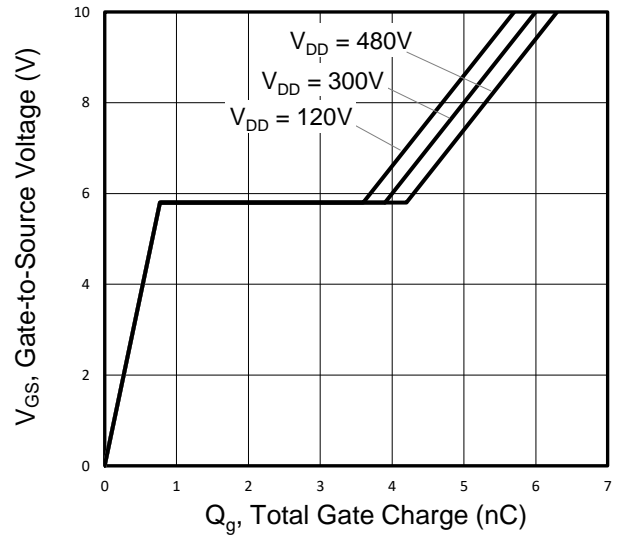
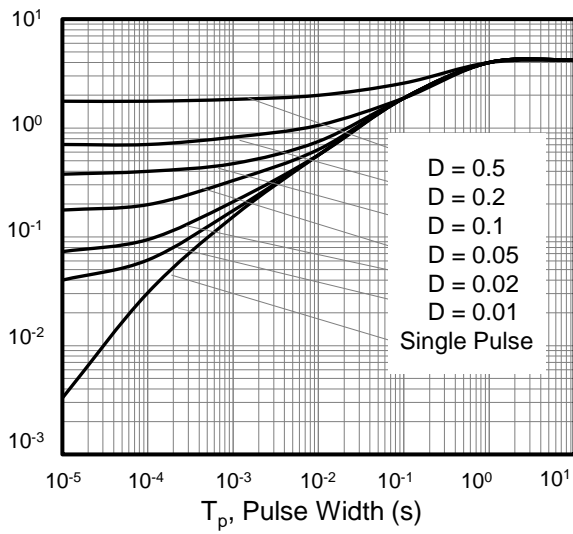
Figure 7. Capacitance

Figure 8. Gate Charge

Figure 9. Transient Thermal Impedance


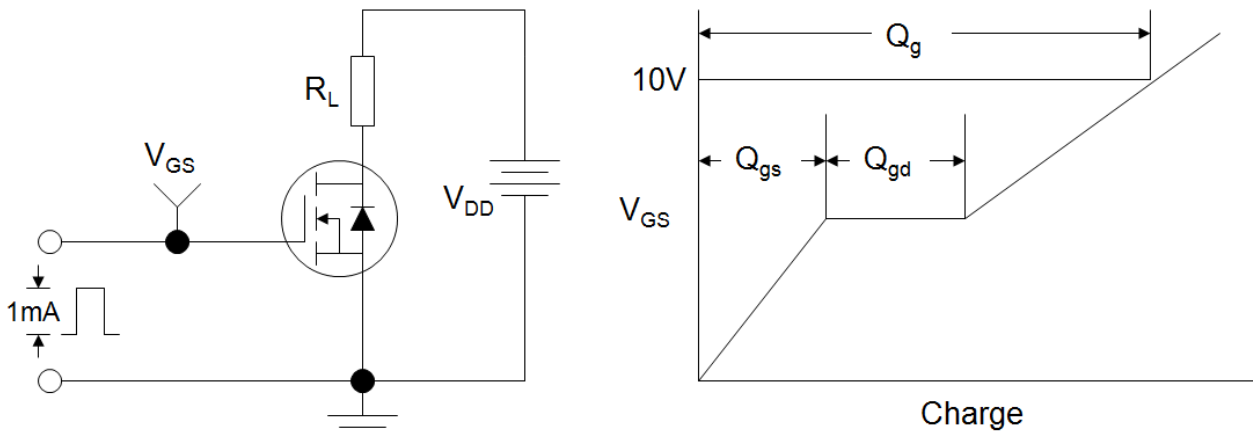
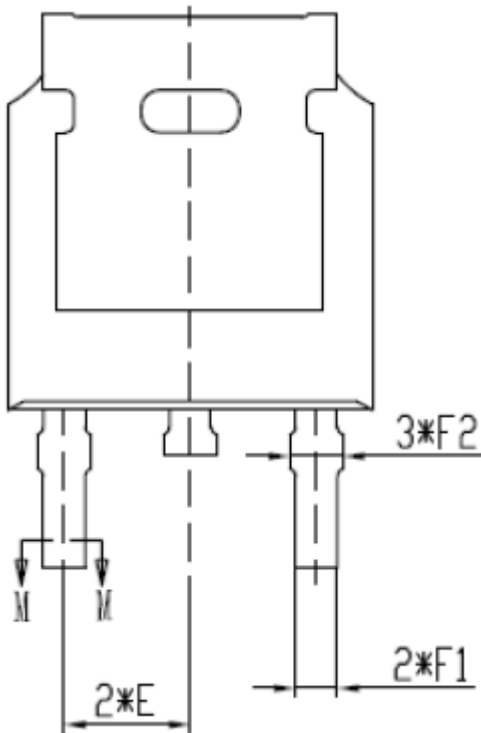
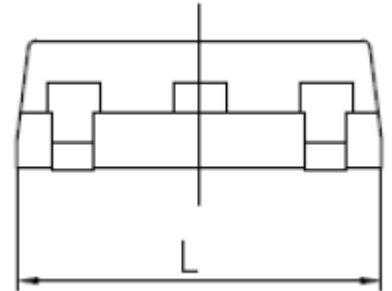
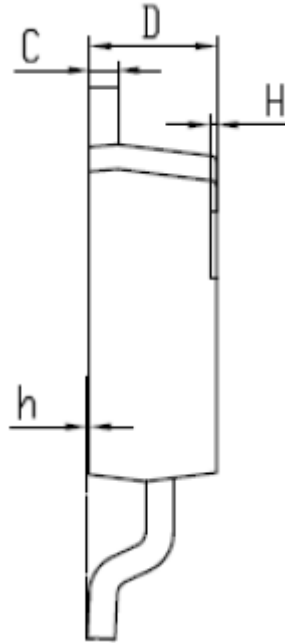
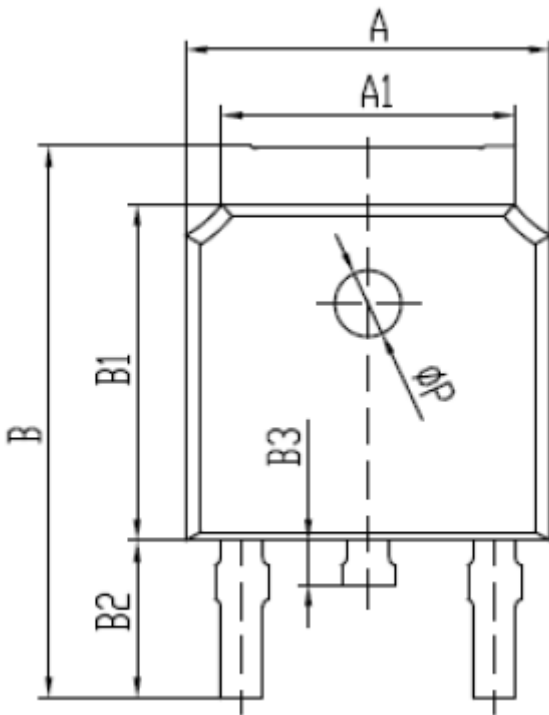
Figure A: Gate Charge Test Circuit and Waveform

Figure B: Resistive Switching Test Circuit and Waveform

Figure C: Unclamped Inductive Switching Test Circuit and Waveform


TO-252


SYMBOLS	MILLIMETERS	
	MIN	MAX
A	6.50	6.70
A1	5.16	5.46
B	9.77	10.17
B1	6.00	6.20
B2	2.60	3.00
B3	0.70	0.90
C	0.45	0.61
D	2.20	2.40
E	2.19	2.39
F1	0.67	0.87
F2	0.76	0.96
H	0.00	0.30
h	0.00	0.13
L	6.50	6.70
ΦP	1.10	1.30

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